

Synthesis of wafer-scale uniform molybdenum disulfide films with control over the layer number using a gas phase sulfur precursor†

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We describe a method for synthesizing large-area and uniform molybdenum disulfide films, with control over the layer number, on insulating substrates using a gas phase sulfuric precursor (H_2S) and a molybdenum metal source. The metal layer thickness was varied to effectively control the number of layers (2 to 12) present in the synthesized film. The films were grown on wafer-scale Si/SiO_2 or quartz substrates and displayed excellent uniformity and a high crystallinity over the entire area. Thin film transistors were prepared using these materials, and the performances of the devices were tested. The devices displayed an on/off current ratio of 10^5 , a mobility of $0.12 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ (mean mobility value of $0.07 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$), and reliable operation.

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Atomically thin-layered materials have been extensively studied in an effort to harness their exotic physical properties that result from two-dimensional confinement.^{1–3} These materials are suitable for practical electronic applications due to their planar geometry.⁴ Graphene, in particular, has been examined for its electronic,^{1,5–8} mechanical,² optical,^{9–11} biological,¹² and chemical properties.¹³ Although graphene shows promise for its utility in high-performance electronics owing to its high charge carrier mobility,¹⁴ it is a zero-bandgap material and suffers from certain drawbacks that prevent its use in electronic devices. For example, it is difficult to use graphene to prepare digital integrated circuits because those devices require a bandgap exceeding 0.4 eV in order to achieve an acceptable performance for practical applications.¹⁵ Some researchers have attempted to increase the bandgap in graphene, but the bandgap size is generally too small, and all efforts occur at the expense of its excellent properties.^{16–19}

Recent studies have explored the use of two-dimensional semiconducting materials, such as sulfides (MoS_2 , WS_2 , TiS_2 , *etc.*) and selenides (GaSe , NbSe_2 , In_2Se_3 , *etc.*), for the preparation of thin film transistors (TFTs) and high-performance integrated circuits because these materials provide an intrinsic bandgap between 1 and 2 eV.²⁰ Molybdenum disulfide (MoS_2), in particular, has attracted attention due to its high mobility, which exceeds $200 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, and its on/off ratio of 10^8 , which can meet the needs of many electronic applications.²¹ In addition to having good electrical properties, atomically thin MoS_2 crystals exhibit excellent mechanical and optical properties that are useful for large-area applications, such as back-plane TFTs for flexible and transparent displays. Utility in large-area synthesis techniques is essential for the application of MoS_2 materials in macroscopic electronic applications. Recently, several approaches based on chemical vapor deposition (CVD) have been reported. One of the most commonly used methods involves the reaction of the Mo metal with the sulfur precursor gas evaporated from a solid sulfur source.²² Although this approach can facilitate the large-area synthesis of MoS_2 films over a length scale of a few inches, it can be difficult to control the number of layers and the uniformity of the layer distribution. Residual Mo atoms tend not to react and remain with the sulfur atoms. Another CVD method involved the use of MoO_3 in place of the Mo metal source. The resulting MoS_2 films exhibited relatively high crystallinity, a low concentration of residual MoO_3 , and reasonable charge carrier mobility.^{23–26} This method has several weaknesses: it is difficult to control the film thickness, and discontinuities are present between the crystals, producing a non-uniform charge carrier mobility.

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Here, we present the development of a large-area synthesis method for preparing high-quality MoS₂ films using H₂S gas and a Mo metal source. This approach offers important advantages over other CVD methods, including (i) uniform layer thicknesses over a large substrate area, such as SiO₂ or quartz, up to the wafer scale and (ii) readily controllable film thickness by changing the thickness of the deposited metal source. We demonstrated the preparation of a large-area transferrable MoS₂ TFT array on a wafer. This technology offers important advantages over transparent semiconducting materials, such as oxide semiconductors.

The synthesis of MoS₂ begins with the deposition of Mo metal on SiO₂/Si or a quartz wafer by e-beam evaporation. The thickness of the molybdenum metal layer was varied from 0.5 to 3 nm using a deposition rate below $\sim 0.1 \text{ \AA s}^{-1}$ under high vacuum conditions. The sample was positioned in the tubular quartz chamber represented in Fig. S1† under a rough vacuum (3.4×10^{-3} Torr) and heated up to 750 °C within a few seconds under the flow of Ar at a rate of 50 standard cubic centimeters per minute (sccm). In a pre-deposited sample, the surface of the molybdenum metal usually becomes oxidized under the atmospheric conditions. Such surfaces could be reduced by instantaneous reaction with hydrogen gas after reaching a temperature of 750 °C. After the pre-annealing process, the H₂S/H₂/Ar (1 : 5 : 50) reaction gas mixture was injected to synthesize MoS₂ over 15 min. The chamber pressure was maintained at 3.1×10^{-1} Torr during the synthesis step. Instantaneous annealing at 1000 °C was found to enhance the crystallinity of MoS₂ films synthesized under a H₂S/Ar (1 : 50) gas mixture flow.²⁷ The sample was rapidly cooled down to room temperature by moving of the furnace from the reaction position under an Ar flow rate of 50 sccm.

The synthesized MoS₂ films were transferred onto a desired substrate using polymer supports, such as poly(methyl methacrylate) (PMMA), polydimethylsiloxane (PDMS), and thermal release tape (TRT), by etching away a SiO₂ sacrificial layer using buffered oxide etchant (BOE). After etching of the SiO₂ layer, polymer/MoS₂ was delivered onto a target substrate. The PMMA support layer was effectively removed using acetone boiled up to 80 °C. The TRT support was detached by removing the attractive force holding the MoS₂ when the 90 °C thermal treatment was performed using a roll to roll laminator.

Raman spectra and photoluminescence (PL) were obtained by confocal Raman microscopic systems (Witec Alpha 300 M+). Wavelength and power of the laser are 532 nm and 2 mW, respectively. Gratings of the spectrometer were 1800 and 600 g

mm⁻¹ for Raman spectra and PL, respectively. The confocal Raman mapping images were obtained by calculating the difference between center values of Lorentz fitting to each Raman mode. UV-vis spectroscopy measurement for a number of layers was conducted with injection of a light source from a deuterium and tungsten lamp (Shimadzu UV-3600). The information of binding energies in MoS₂ was characterized by using an X-ray photoelectron spectrometer (XPS, Phi V5000). XPS measurements were performed with an Al K α X-ray source on the samples. The energy calibrations were made against the C 1s peak to eliminate the charging of the sample during analysis. The TEM samples were prepared using the transfer process of 2-layered MoS₂ onto the Cu grid. To avoid residue on the top surface of MoS₂, a polymer support layer was not employed in this process. TEM images were taken by using a FEI Titan Cube with acceleration voltage of 80 kV.

The 4-layered MoS₂ film was transferred onto an SiO₂/Si substrate which includes Au source and drain electrodes. The channel area was defined by the photolithography process with oxygen reactive ion etching. After fabricating the bottom gate structure, the device was annealed to improve contact resistance between MoS₂ and Au. Then a 30 nm HfO₂ layer was deposited on the entire area by the atomic layer deposition (ALD) process. TDMAH was used as an Hf precursor and H₂O was used as an oxidant. The substrate temperature was maintained at 250 °C during the entire ALD process. A top gate electrode overlapping the whole channel area was formed by thermal evaporation with the lift off pattern made by the photoresist.

Fig. 1 illustrates the mechanism of synthesis and transfer of the large-area MoS₂ film. The injected H₂S gas, which was used as a sulfur precursor, decomposed more readily than the evaporated solid sulfur source because the solid sulfur was evaporated at an even lower temperature, thus maintaining the aggregated states until adsorption onto the metal source. These processes resulted in poor film thickness uniformity.²² However, H₂S precursor gas may be homogeneously adsorbed onto and reacted with the molybdenum surface without inducing aggregation, thus resulting in uniform quality of MoS₂ films. A sufficiently long sulfurization time under H₂S gas flow at 750 °C can result in the entire conversion of molybdenum into a MoS₂ layer; therefore, the synthesized layer thickness could be effectively controlled according to the thickness of the pre-deposited molybdenum layer. The synthesized film manifests an atomically flat surface indicated by the RMS surface roughness value of ~ 0.20 nm from atomic force microscopy

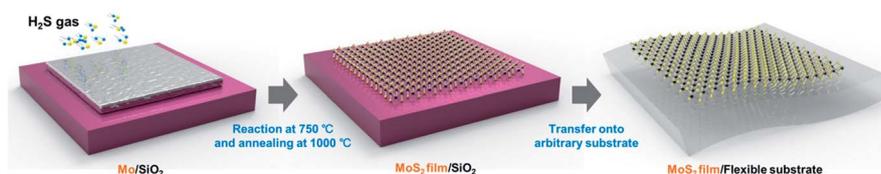


Fig. 1 Schematic illustration of the growth and transfer process system used to prepare MoS₂ films. Large-area MoS₂ films could be synthesized on a SiO₂/Si wafer or a quartz substrate using chemical vapor deposition processes, and the film could be transferred to an arbitrary substrate using a transfer process with a polymer support.

(AFM) (see Fig. S2†). The change of the sulfur precursor from solid sulfur²² to H₂S gas, which can be considered as an incremental modification, was critical in making such a difference of almost perfect uniformity and precise thickness control of the synthesized film.

The synthesized MoS₂ film was uniformly transferred to a two-inch wafer substrate without creating visible cracks or wrinkles (Fig. 2a). Optical microscopy images of a 2 layer MoS₂ film on a SiO₂/Si substrate revealed a uniform color distribution (Fig. 2b). Transferred MoS₂ films do not show surface residue which could be originated from sulfuric precursors for the synthesis process and polymer supports for the transfer process. Fig. 2c and d show MoS₂ films on a transparent quartz substrate and a PET film. Transparent MoS₂ films were formed on quartz substrates by direct synthesis or on a plastic substrate by a wet transfer process. The letters “M”, “o”, “S”, and “2” in Fig. 2a were formed from 2, 4, 8, and 12 layers of the MoS₂ film, respectively, which were offset against a dark level background. Once a molybdenum metal layer with 0.5, 1, 2, or 3 nm thickness had been deposited in the shape of the corresponding character, a MoS₂ film having a distinct number of layers could be synthesized by controlling the layer deposition mechanism, as described above. The synthesis result reveals that each layer of MoS₂ is synthesized from 0.25 nm of Mo metal. The MoS₂ films on the quartz substrates exhibited a high transparency, even for a 12 layer sample. The film prepared on the plastic substrate, which was formed from 4 layers, displayed a high flexibility. This suggests that even thick MoS₂ could be used in transparent and flexible electronics for the development of future device applications.

The qualities of the synthesized MoS₂ films having different numbers of layers were characterized using optical analysis methods, including Raman, photoluminescence (PL), and UV-vis spectroscopy. The Raman spectra revealed the peak

positions of the E_{2g}¹ and A_g¹ modes corresponding to in-plane and out-of-plane vibrations, as a function of the number of layers, respectively (Fig. 3a). The positions of the E_{2g}¹ and A_g¹ modes in the MoS₂ film were determined based on the vibrational energies of each mode, which were affected by the number of layers present. In general, the E_{2g}¹ vibration softened, whereas the A_g¹ vibration stiffened at higher numbers of layers. These trends allowed the shift in the Raman frequencies of each mode to be used as indicators of the number of layers present.²⁸ According to the spectra shown in Fig. 3a, the relative peak position differences between the modes varied from 20 to 26 cm⁻¹, which corresponded to 2 to 12 layered MoS₂ films, according to a previous study.^{28,29} The PL response in the energy range corresponding to a direct transition indicated semiconducting behavior in the 2H-MoS₂ films.^{30,31} In general, the relative intensity of the PL band decreased as the number of MoS₂ layers present increased because the indirect electron transitions dominated the direct transitions. The direct electron transition wavelength was shifted to higher values at higher numbers of MoS₂ layers.³⁰ The PL response of the synthesized MoS₂ film was represented by the intensity decay. The transition wavelength was red-shifted in samples having a higher number of layers (Fig. 3b). Fig. 3c shows the UV-vis transmittance spectra of 2, 4, 8, and 12 layered MoS₂ films. The peak positions in each MoS₂ film corresponded to the band structure. The peak positions shifted to lower energies as the number of layers in the PL spectra increased.³¹ The transmittance values in the visible region (550 nm) were 96.7% (2 layers), 89.2% (4 layers), 69.4% (8 layers), and 48.4% (12 layers). These values were inversely proportional to the number of layers.

The crystal lattice structure and quantity of the residual, unreacted Mo metal present were characterized using XPS techniques (Fig. 4a). In general, hetero-atomic bonds involving Mo atoms yield bonding energies that depend on the crystal structure and atomic composition. 1T-MoS₂ and 2H-MoS₂ displayed different binding energies for each orbital in the Mo and S centers, yielding, respectively, metallic or semiconducting behavior. The 2H-MoS₂ sample displayed a shift in the Mo orbital binding energy to 229.5 and 232.6 eV, which corresponded to the Mo 3d_{5/2} and Mo 3d_{3/2} orbitals, respectively.³² The negligible peak intensity at the initial binding energy of the Mo orbital indicated that complete sulfurization of the Mo atoms had taken place to form the 2H-MoS₂ crystalline structure. The binding energies of the S atom orbitals further supported the formation of the 2H-MoS₂ crystal structure. Therefore, the MoS₂ sample synthesized using this method exhibited a completely sulfurized lattice that had been converted to a 2H-structure without yielding residual Mo atoms or a 1T-structured MoS₂ phase.³⁰ The concentration of MoO₃ in the MoS₂ lattice is important because these moieties can act as trap centers for charge carriers passing through the MoS₂ layer. The negligible intensity of the MoO₃ binding energy peak at 232.8 and 235.6 eV indicated that the low pressure and the Ar/H₂ atmosphere prevented the formation of MoO₃ during the synthesis process.³³

A more detailed structural analysis was performed using high-resolution TEM (HRTEM). The HRTEM image shown in

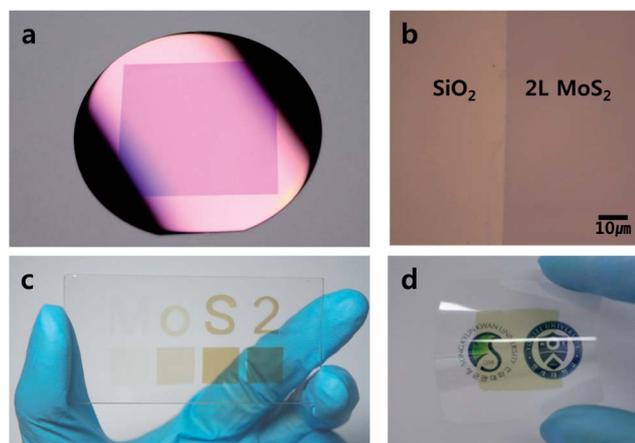


Fig. 2 Large-area, uniform MoS₂ films prepared on different substrates. (a) Wafer-scale MoS₂ films synthesized by CVD processes and transferred using a polymer support. (b) Microscopic image of a uniform 2-layered MoS₂ film. (c) The characters “M”, “o”, “S”, and “2” were prepared from 2, 4, 8, and 12 layered MoS₂ films, respectively. (d) MoS₂ film transferred onto a PET substrate, demonstrating mechanical bendability.

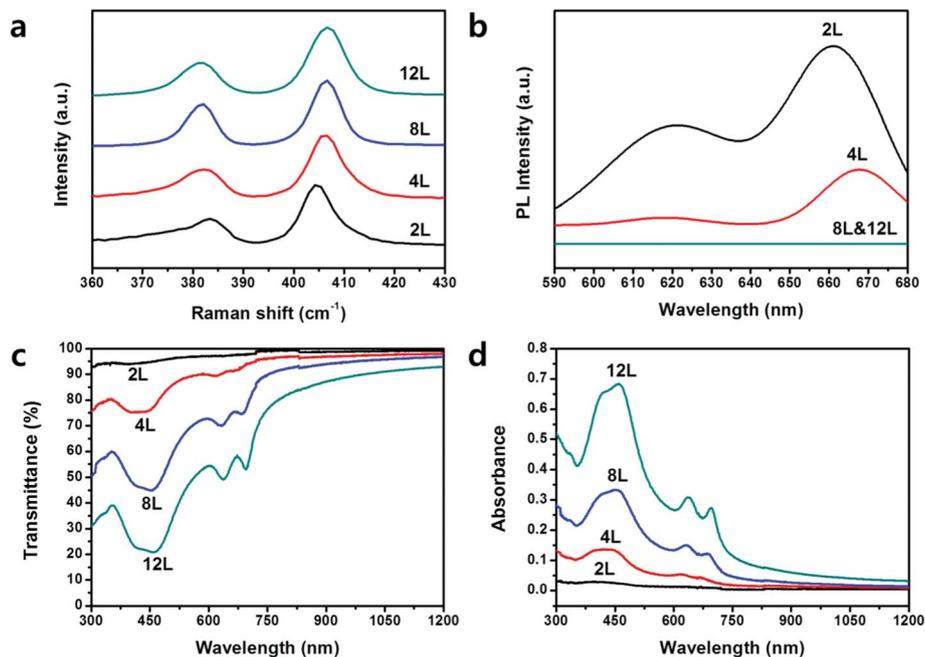


Fig. 3 Optical characterization of films prepared with different layer numbers. (a) The Raman spectra of the MoS₂ films prepared with layers increasing from 2 to 12. (b) Photoluminescence spectra of the MoS₂ films having different thickness values using a 532 nm excitation laser. (c and d) UV-vis transmittance and absorbance spectra of the MoS₂ films as a function of the layer number.

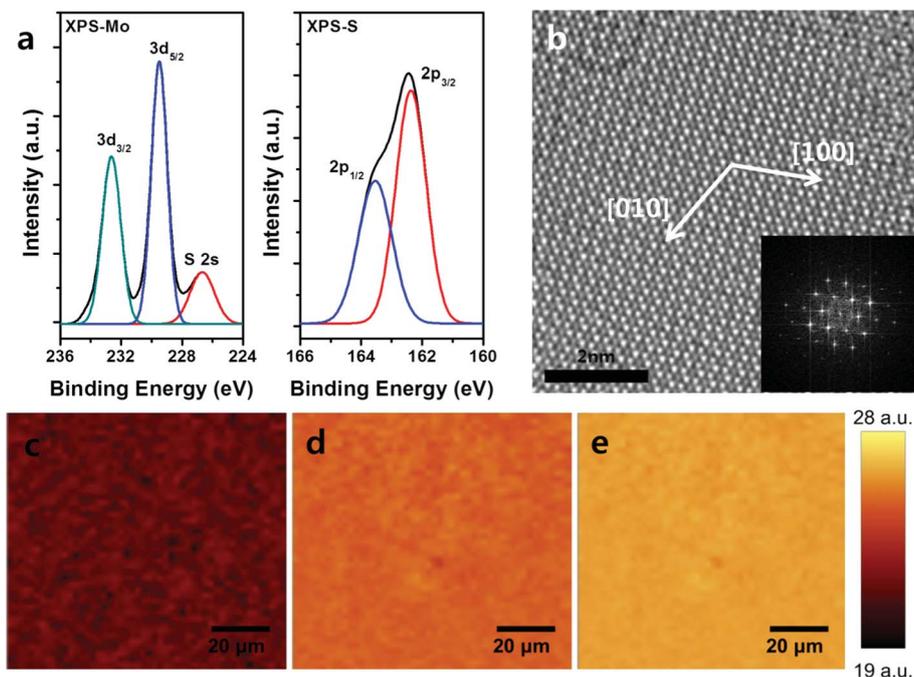


Fig. 4 Structural characterization of the synthesized films. (a) X-ray photoluminescence spectroscopy (XPS) was used to analyze the binding energies of the Mo and S atoms in the synthesized MoS₂ film. (b) High-resolution transmission electron microscopy (HRTEM) images of the synthesized MoS₂ film (the inset shows the diffraction pattern of the electron-transmitting area). (c–e) Confocal Raman microscopy images of the 2-, 4-, and 12-layered MoS₂ films representing a large-area uniform distribution.

Fig. 4b indicates a high crystallinity in the synthesized MoS₂ film, which was also inferred from the corresponding diffraction pattern shown in the inset of Fig. 4b. The diffraction patterns indicated a crystalline direction indicated by the arrow

in Fig. 4b. 2H-Structured synthesis appeared to proceed along the planar direction. The crystalline domain size in the synthesized MoS₂ films was determined to be around 10 nm based on the HRTEM images (see Fig. S3†).

The controllability over the number and uniformity of the layers was examined by conducting Raman mapping studies over a few hundred micrometers in the synthesized 2, 4, and 12 layered MoS₂ films. Fig. 4c–e show two-dimensional Raman maps of the peak position differences between the A_g¹ and E_{2g}¹ modes for three different MoS₂ films having different thickness values. The color distributions in the Raman mapping images showed almost perfect uniformity over the large areas. These results indicated that the synthesis method described here can provide highly uniform thickness-controlled films.

The surface morphologies were characterized using AFM techniques, and the roughness values were found to be comparable to the values for the substrate itself. The thickness variation was found to be negligible, indicating that the films were highly uniform, as revealed in the Raman spectroscopy measurements. The number of layers of the film was checked again by using AFM. The measured step heights of 2L, 4L, 8L, and 14L of MoS₂ films corresponded to the theoretical thicknesses, revealing that the results agreed with the Raman spectroscopy measurements (see Fig. S4†).

Top-gated TFT-arrays (~4000 devices) were fabricated using a 4 layered MoS₂ film as the channel material, mounted on a 2 inch SiO₂/Si substrate and prepared using conventional photolithography and etching processes (Fig. 5a). The device arrays were prepared with a variety of channel geometries, where the length was varied from 10 to 25 μm and the width was varied from 100 to 200 μm. Source–drain (S–D) and gate (G) contact electrodes were formed by evaporating the Au metal.

A gate dielectric layer comprising a 30 nm thick HfO₂ layer was deposited using atomic layer deposition (ALD) methods. The devices were annealed to enhance the electrical contact at 300 °C in a vacuum environment. Fig. 5b and c show the electrical characteristics of a representative device having a channel length of 15 μm and a width of 200 μm. The electron mobility and the on/off current ratio were 0.12 cm² V⁻¹ s⁻¹ and 10⁵ respectively. The linearity of the I_{ds}–V_{ds} characteristic curves indicated ohmic contact between the film and the electrodes. Fig. 5d shows a statistical distribution of the electron mobilities measured in an array of 180 FET devices. Over 90% of all devices exhibited stable operation, with a mean value of 0.07 cm² V⁻¹ s⁻¹. The low mobility and failure of the devices appeared to be due to the small crystal domain size and the boundary properties, which may act as charge scatterers. We speculate that by controlling annealing time, annealing environment, or the substrate material the film quality can be improved.

In conclusion, we demonstrated the large-area synthesis of uniform and transparent MoS₂ films, with control over the number of synthesized layers, using CVD methods involving gas and metal precursors. The thickness of the synthesized MoS₂ film could be controlled by depositing Mo layers having different thickness values. The synthesized MoS₂ films were characterized by Raman/PL/UV-vis spectroscopy, AFM, XPS, and HRTEM. All analyses revealed almost perfect film uniformity, precisely controlled film thickness values, and high film crystallinity without the presence of an amorphous phase. TFT arrays prepared from the synthesized films were used to

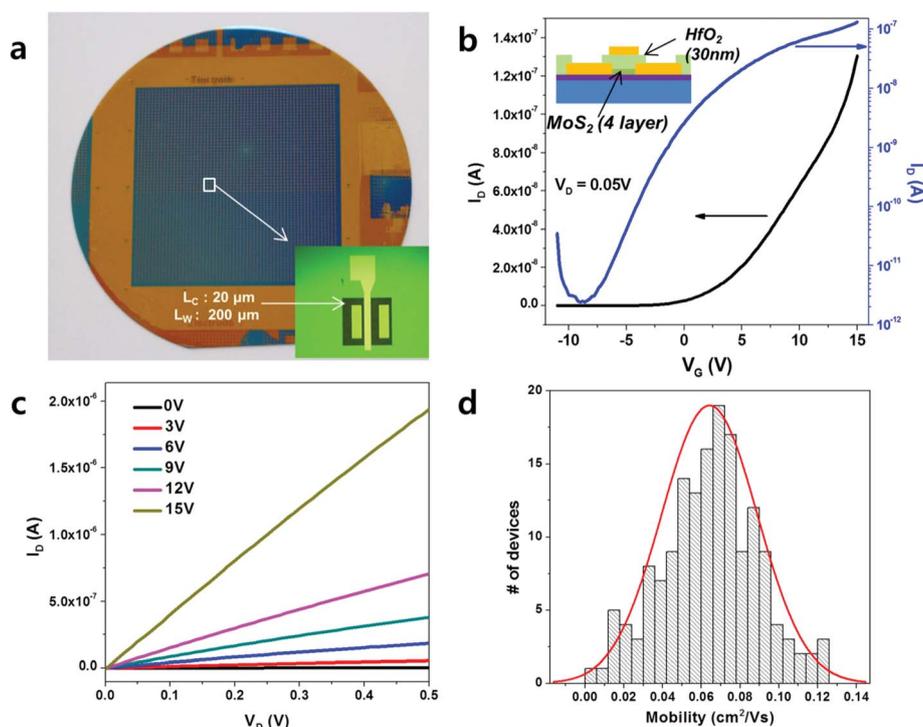


Fig. 5 Electrical characterization of the arrays of MoS₂ FET devices. (a) Photographic image of a MoS₂ top-gate transistor array on a two-inch SiO₂/Si wafer. (b) Transport characteristics of top-gate MoS₂ devices under a drain voltage of 50 mV. The inset denotes the device cross-section. (c) The output characteristics of the top-gate CVD-MoS₂ devices under various gate bias values. (d) Histogram of the mobility distributions obtained from the large-area MoS₂ devices.

demonstrate the stable operation of devices (>90% stability), with a mobility of $0.12 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ for the best devices and a high on/off current ratio of 10^5 . We judge that use of the gas phase sulfur precursor is the key factor to thickness controllability and film uniformity. This synthesis method may be applied to other types of metal chalcogenides. The films are useful for the preparation of large-area devices, such as thin film transistors for use in transparent and/or flexible displays, sensors, and solar cells.

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